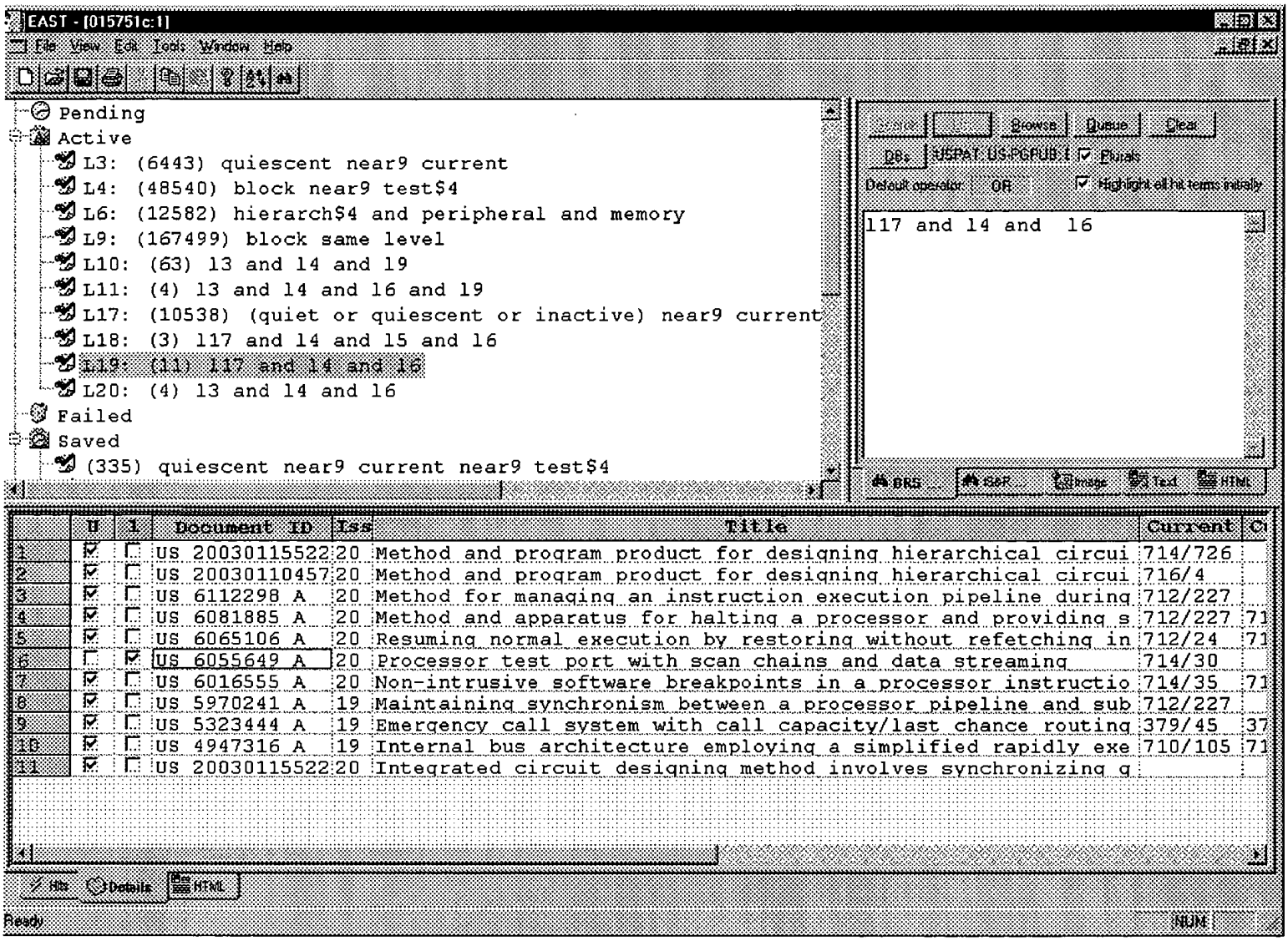
EAST - [015751c:1]
File View Edit Tools Window Help
Pending
Active
L3: (6443) quiescent near9 current
L4: (48540) block near9 test$4
L6: (12582) hierarch$4 and peripheral and memory
L9: (167499) block same level
L10: (63) l3 and l4 and l9
L11: (4) l3 and l4 and l6 and l9
L17: (10538) (quiet or quiescent or inactive) near9 current
L18: (3) l17 and l4 and l5 and l6
L19: (11) l17 and l4 and l6
L20: (4) l3 and l4 and l6
Failed
Saved
(335) quiescent near9 current near9 test$4
Browse
Queue
Clear
DBs
USPAT,US-PGPUB,I
Plurals
Default operator:
OR
Highlight all hit terms initially
l17 and l4 and l6
BRS
IS&R
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1 US 20030115522 20 Method and program product for designing hierarchical circui 714/726
2 US 20030110457 20 Method and program product for designing hierarchical circui 716/4
3 US 6112298 A 20 Method for managing an instruction execution pipeline during 712/227
4 US 6081885 A 20 Method and apparatus for halting a processor and providing s 712/227 71
5 US 6065106 A 20 Resuming normal execution by restoring without refetching in 712/24 71
6 US 6055649 A 20 Processor test port with scan chains and data streaming 714/30
7 US 6016555 A 20 Non-intrusive software breakpoints in a processor instructio 714/35 71
8 US 5970241 A 19 Maintaining synchronism between a processor pipeline and sub 712/227
9 US 5323444 A 19 Emergency call system with call capacity/last chance routing 379/45 37
10 US 4947316 A 19 Internal bus architecture employing a simplified rapidly exe 710/105 71
11 US 20030115522 20 Integrated circuit designing method involves synchronizing g
Hits
Details
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Ready
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